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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/512,497		02/24/2000	Daniel M. Kinzer	IR-1649(2-1939) 5663			
2352	7590	02/19/2004		EXAMINER			
OSTROLE	OSTROLENK FABER GERB & SOFFEN						
1180 AVENUE OF THE AMERICAS NEW YORK, NY 100368403				ART UNIT	PAPER NUMBER		
	•			2826	-		

DATE MAILED: 02/19/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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-	Application No.	A	pplicant(s)	(**				
	09/512,497	κ	INZER ET AL.					
Office Action Summary	Examiner	Α	rt Unit					
	A. Sefer	2	826					
The MAILING DATE of this communication ap Period for Reply	pears on the cover	sheet with the corr	respondence ad	dress				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut - Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b). Status	136(a). In no event, howe oly within the statutory min will apply and will expire s e, cause the application to	ver, may a reply be timely imum of thirty (30) days wi SIX (6) MONTHS from the become ABANDONED (filed III be considered timely mailing date of this constitution of the constitution	/. ommunication.				
1) Responsive to communication(s) filed on 10 N	November 2003.							
2a) This action is FINAL . 2b) This	action is non-fina	l.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
4) ⊠ Claim(s) <u>22-43</u> is/are pending in the application 4a) Of the above claim(s) is/are withdrases 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>22-43</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or	awn from considera							
Application Papers								
9) The specification is objected to by the Examina	er.							
10) The drawing(s) filed on is/are: a) acc	cepted or b)⊡ obj	ected to by the Exa	aminer.					
Applicant may not request that any objection to the								
Replacement drawing sheet(s) including the correct								
11)☐ The oath or declaration is objected to by the E	xaminer. Note the	attached Office Ad	ction or form PT	O-152.				
Priority under 35 U.S.C. §§ 119 and 120								
12) Acknowledgment is made of a claim for foreignal All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureat * See the attached detailed Office action for a list 13) Acknowledgment is made of a claim for domest since a specific reference was included in the first 37 CFR 1.78. a) The translation of the foreign language profile. Acknowledgment is made of a claim for domest reference was included in the first sentence of the service of the ser	nts have been recents have been recents have been recently documents have (PCT Rule 17.2 tof the certified contic priority under 3 rest sentence of the rovisional applications of the priority under 3 to pri	ived. ived in Application ave been received (a)). pies not received. 5 U.S.C. § 119(e) of specification or in on has been received. 5 U.S.C. §§ 120 ar	No in this National (to a provisional an Application ved. nd/or 121 since	l application) Data Sheet. a specific				
Attachment(s)								
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 	5) 🔲	Interview Summary (PT Notice of Informal Pate Other:						

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 11/10/2003 has been entered; claims 1-21 have been cancelled and new claims 22-43 have been added.

Claim Rejections - 35 USC § 112

- 2. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claims 22 and 36 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The limitation "said conductive strip being <u>narrow</u> such that it makes contact <u>only</u> with a portion of each of said gate electrodes" is not clearly disclosed in the specification to enable one skilled in the art to make and/or use the invention. Without this information it would take undue experimentation to make and use the claimed invention.

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Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 22-35, as understood, are rejected under 35 U.S.C. 103(a) as being unpatentable over Kinzer USPN 6,476,443 in view of Hshieh et al. USPN 5,763,915.

Kinzer discloses in figs. 5-8 a MOSgated device comprising a semiconductor body 80 of a first conductivity type; a channel region 82 of a second conductivity type formed in said semiconductor body; a conductive region 83 or source region (as in claim 24) of said first conductivity type formed in said semiconductor body and extending from major surface of said semiconductor body to at least said channel region; a plurality of spaced trenches 85 parallel to one another and coextensive with one another (as in claim 30) or are formed in a plurality of spaced rows and are parallel to one another and coextensive with one another within each row (as in claim 31) extending into said semiconductor body below said channel region, each of said trenches being adjacent a mesa and each terminating at a contact region in semiconductor body, said channel region and said conductive region extending into said contact region, and said conductive region uninterruptedly extending between each two adjacently disposed trenches; a gate insulation layer 90 comprising oxide (as in claim 26) and having a thickness greater than about 200 Å fully covering the interior of each said trenches (as in claim 35) dispose over the sidewalls and bottom of each of said trenches; a gate electrode 95 comprising polysilicon (as in claim 25) formed in each of said trenches over said gate insulation layer; and a remote contact 71 Application/Control Number: 09/512,497

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or source contact (as in claim 29) formed over at least said contact region and in electrical contact with at least said conductive region, wherein said remote contact extends through said conductive region to make contact with said channel region below said conductive region (as in claim 23), but does not disclose a conductive strip extending transverse to each of said trenches.

Hshieh et al disclose in figs. 4A, 4C a MOSgated device comprising a conductive strip 130' extending transverse to a plurality of trenches and electrically connected to each of gate electrodes 120, said conductive strip being narrow such that it makes contact only with a portion of said gate electrodes.

Therefore, it would have been obvious to one skilled in the art at the time the invention was made to incorporate the teachings of Hshieh et al with Kinzer's device since that would eliminate the potential weak points of early breakdown as taught by Hshieh et al.

As for claim 27, Kinzer discloses an epitaxial layer 81 formed over a silicon substrate of the same conductivity and further comprising a second contact 73 or drain contact (as in claim 28) in electrical contact with said substrate.

As for claims 32-34, Kinzer discloses (see col. 2, lines 1-16) trenches having a depth of about 1.8 microns and extend to about 0.2 to 0.25 microns below said channel region (as in claim 33), wherein said trenches have a width of about 0.6 microns and a spacing of a bout 0.6 microns or greater (as in claim 34).

6. Claims 36-43, as understood, are rejected under 35 U.S.C. 103(a) as being unpatentable over Kinzer USPN 6,476,443 in view of Hshieh et al. USPN 5,763,915.

Kinzer discloses in figs. 5-8 a MOSgated device comprising an epitaxial silicon body of a first conductivity type; a channel region 82 of said first conductivity type formed in said epitaxial

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silicon body, a source region 83 of said first conductivity type formed in said epitaxial silicon body and extending from a first major surface of said silicon body to at least said channel region; a plurality of spaced trenches 85 parallel to one another and coextensive with one another (as in claim 39) or are formed in a plurality of spaced rows and are parallel to one another and coextensive with one another within each row (as in claim 40) extending into said silicon body below said channel region, each of said trenches being adjacent a mesa and each terminating at a source contact region in said silicon body, said channel region and said source region extending into said source contact region, and said source region uninterruptedly extending between each two adjacently disposed trenches; a gate oxide layer 90 dispose over the sidewalls and bottom of each of said trenches; a polysilicon gate electrode 95 formed in each of said trenches over said gate oxide layer; and a remote source contact formed over at least said source contact region and in electrical contact with at least said source region, wherein said remote source contact extends through said source region to make contact with said channel region below said source region (as in claim 37), but does not disclose a conductive strip extending transverse to each of said trenches.

Hshieh et al disclose in figs. 4A, 4C a MOSgated device comprising a conductive strip 130' extending transverse to a plurality of trenches and electrically connected to each of gate electrodes 120, said conductive strip being narrow such that it makes contact only with a portion of said gate electrodes.

Therefore, it would have been obvious to one skilled in the art at the time the invention was made to incorporate the teachings of Hshieh et al with Kinzer's device since that would eliminate the potential weak points of early breakdown as taught by Hshieh et al.

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As for claim 38, Kinzer discloses a silicon substrate and a drain contact 73 in electrical connection with said substrate, said epitaxilly silicon substrate being formed over said substrate.

As for claims 41-43, Kinzer discloses (see col. 2, lines 1-16) trenches having a depth of about 1.8 microns and extend to about 0.2 to 0.25 microns below said channel region (as in claim 33), wherein said trenches have a width of about 0.6 microns and a spacing of a bout 0.6 microns or greater (as in claim 34).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A. Sefer whose telephone number is (703) 605-1227.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (703) 308-6601.

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January 16, 2004